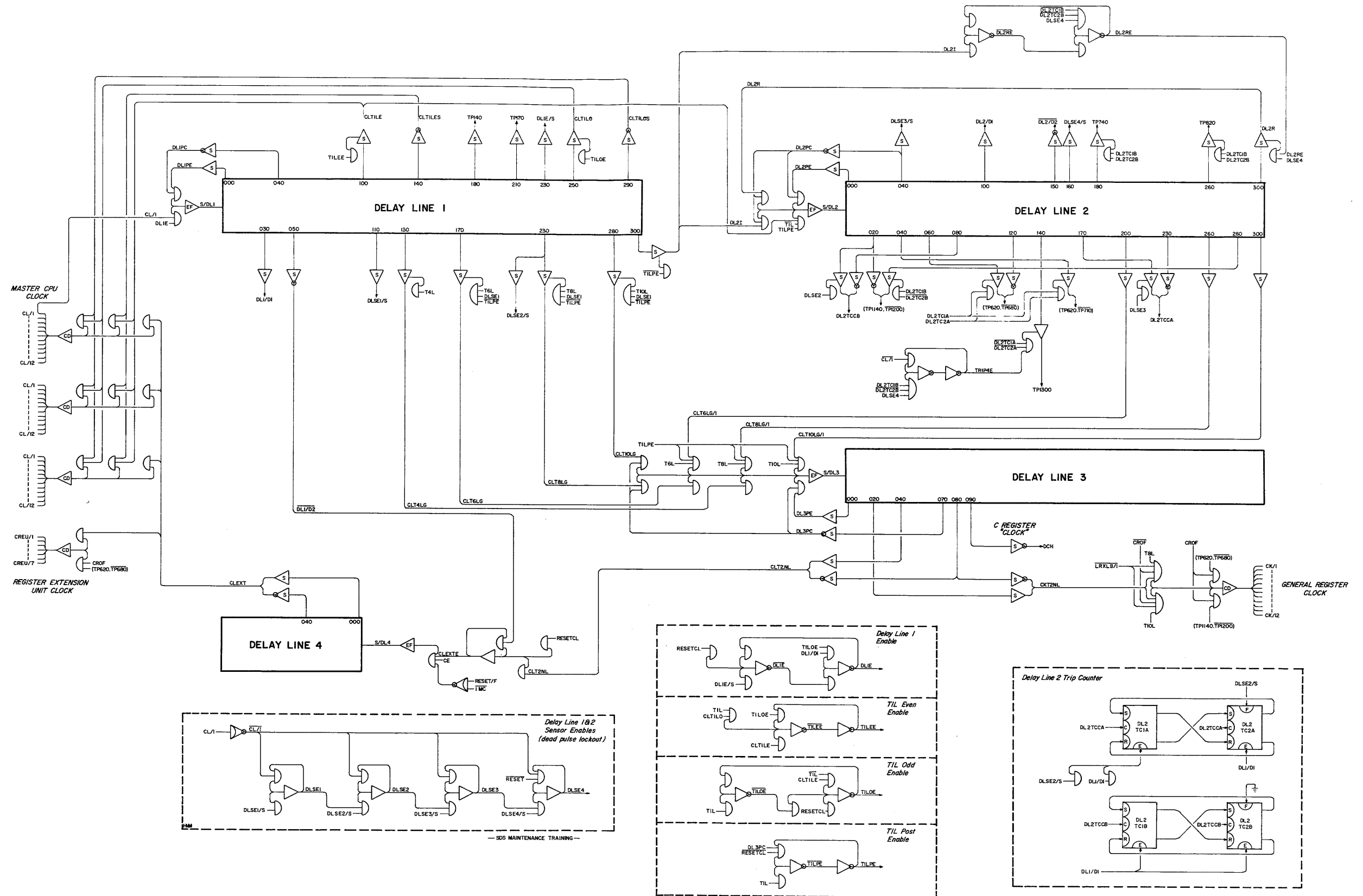


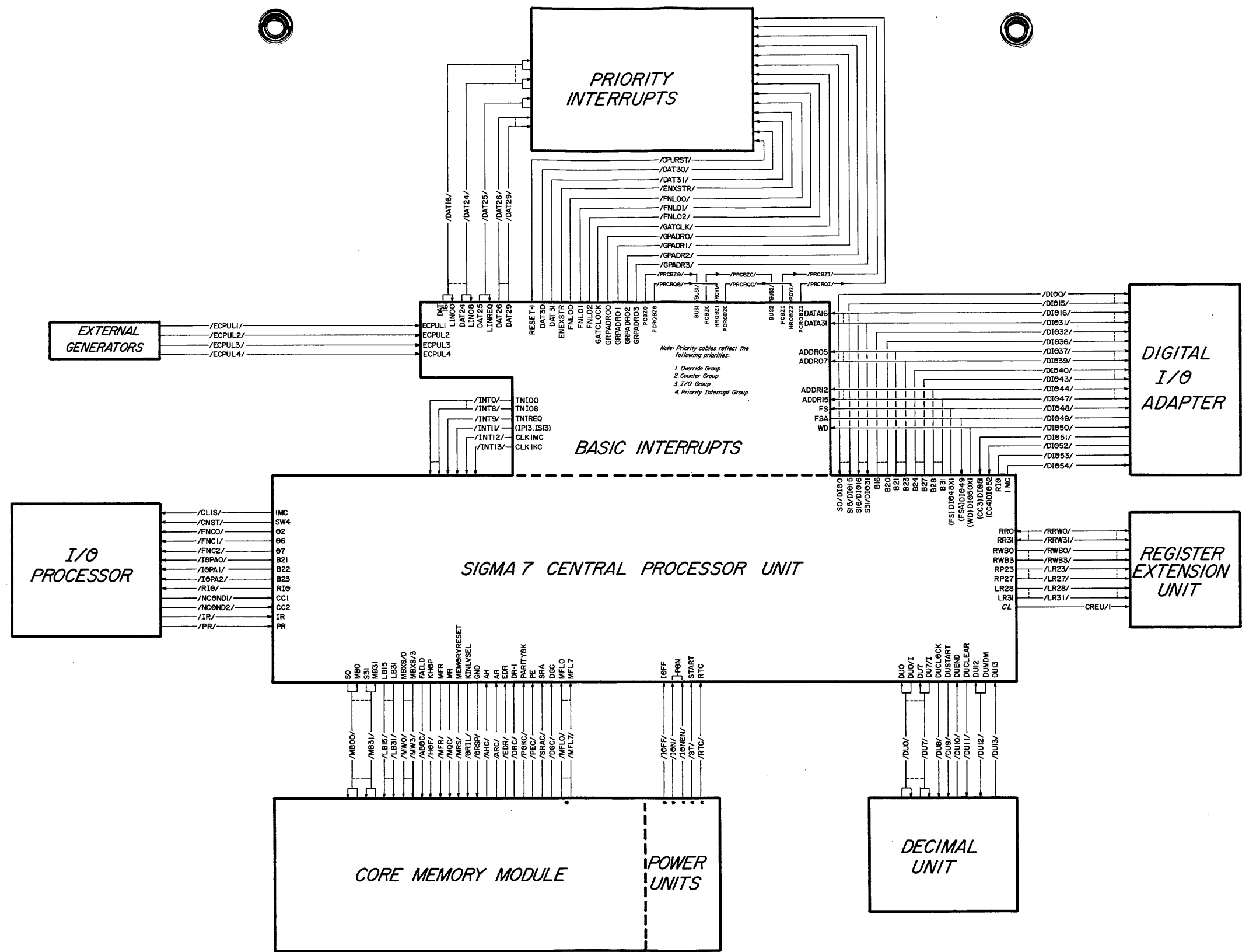
SIGMA 7 CENTRAL PROCESSOR

FUNCTIONAL DIAGRAM

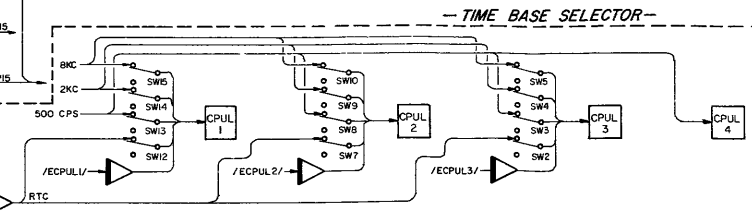
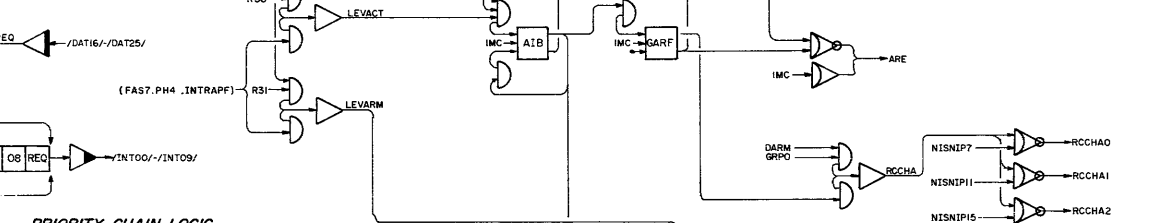
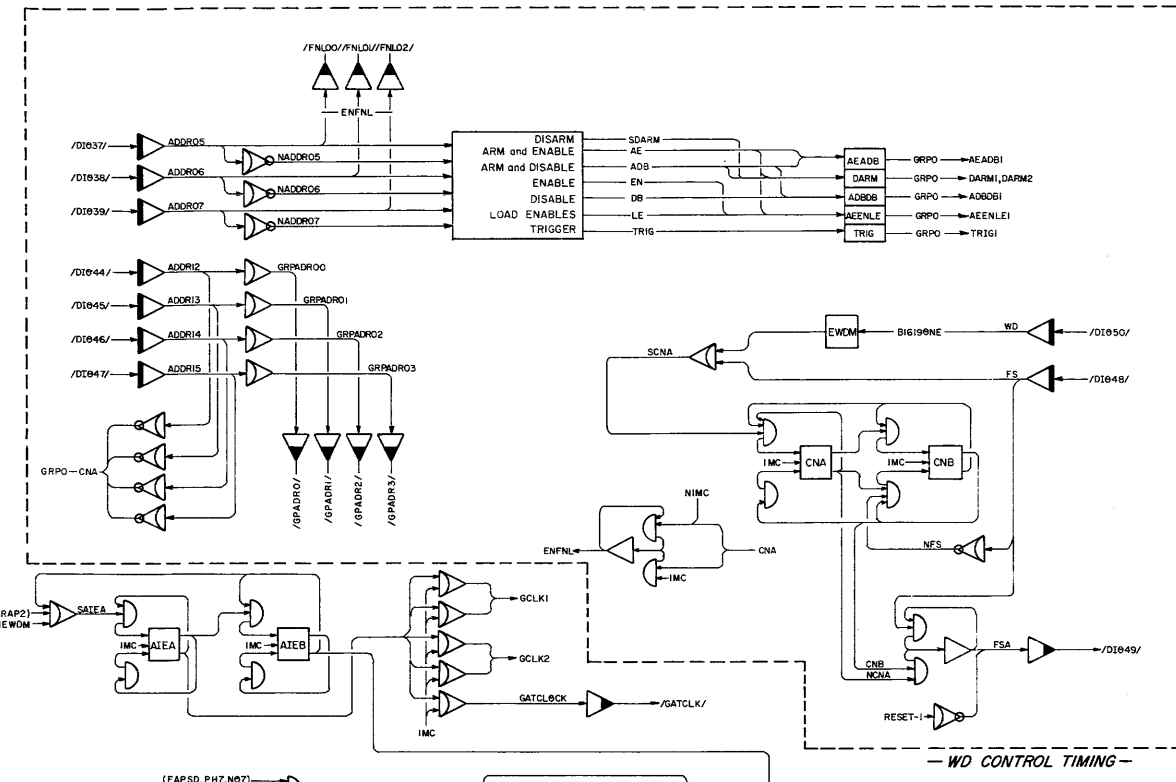
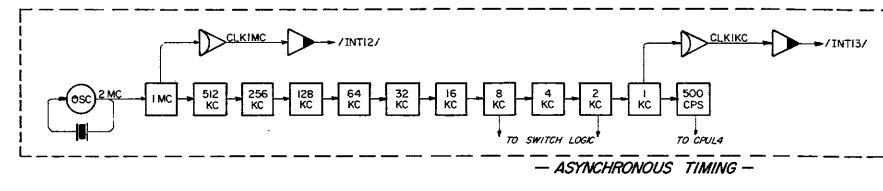
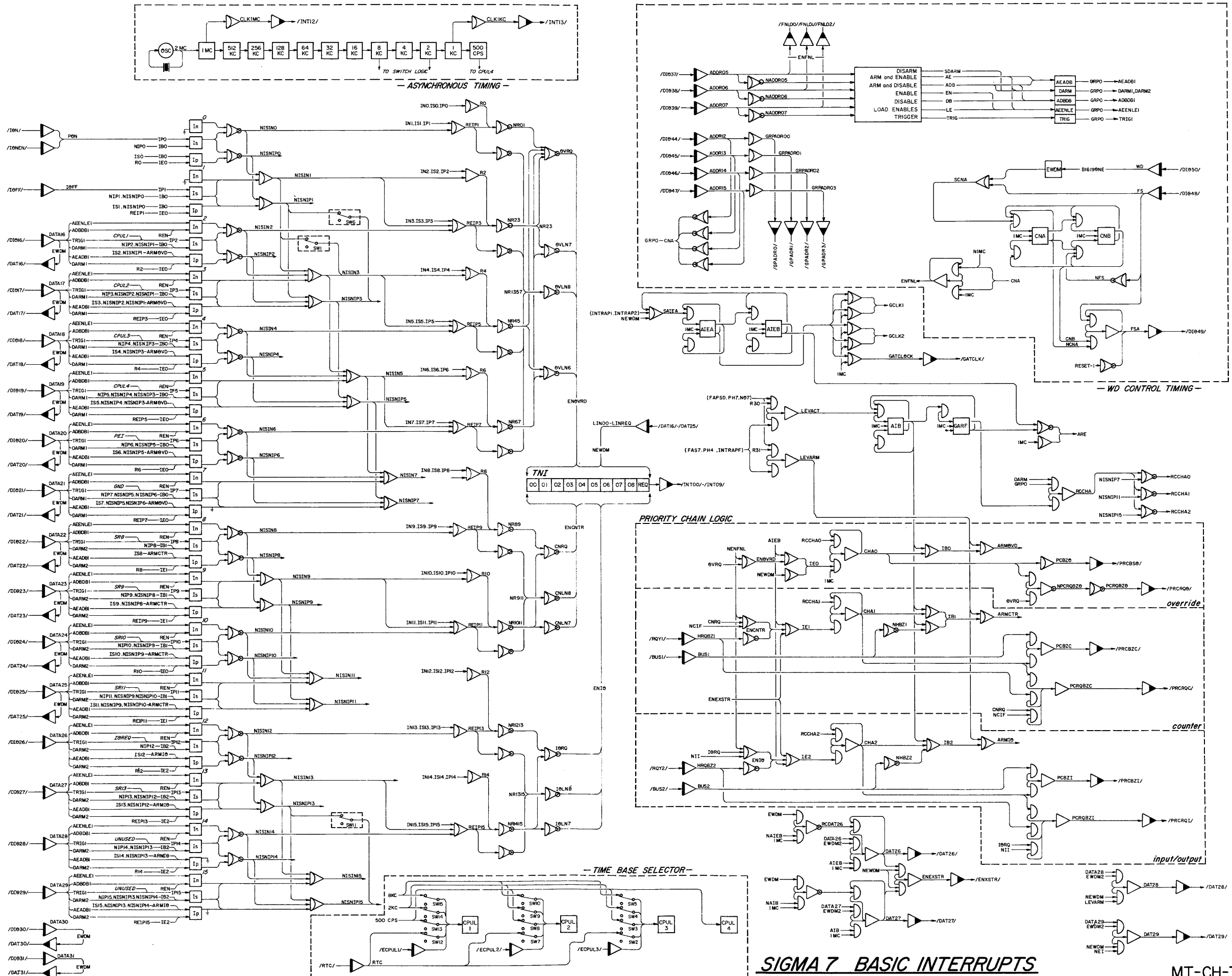


(REVISION K)

SIGMA 7 CLOCK SYSTEM



SIGMA 7 INTERFACE SIGNALS



SIGMA 7 BASIC INTERRUPTS

MT-CH-7-4